

Home	Invited Speakers
Program	Abstract submission
Registration	Hotels
Banquet	Contact

Updated information

2016/9/20	<b>NEW</b> Home page open!!
2016/10/5	<b>NEW</b> Invited speakers are updated!!
2016/10/20	<b>NEW</b> Time schedule is uploaded!!
2016/11/4	<b>NEW</b> Detailed program is uploaded (all workshops)!!

Workshop information

All workshops are in series between November 16, 2016 - November 20, 2016 and detailed information are described below. Registration and abstract submission pages are in common for all workshops.

JSPS-EPSRC Tohoku-Cambridge-CNRS Core to Core program Symposium "Two dimensional electronics/spintronics devices"  
Chair:Tetsuo Endoh, John Robertson, and Pierre Seneor

Date

November 16, 2016 - November 17, 2016 (17th P.M. is the joint workshop with RIEC workshop on spintronics)

Workshop description

This workshop will cover especially spintronics and advance materials such as 2D materials, Graphene, Si-related materials, III-V semiconductor heterostructures, ferromagnetic metals, their integrated device technologies and etc.

**14th RIEC workshop on spintronics****Chair:**Hideo Ohno**Date**

November 17, 2016 - November 19, 2016 (17th P.M. is the joint workshop with JSPS-EPSRC Tohoku-Cambridge-CNRS Core to Core program)

**Workshop description**

This workshop will cover a wide range of recent progress in spintronics, from fundamental physics to devices for applications. Top researchers from all over the world are invited to present a comprehensive overview of what is happening in this field.

**4th JSPS Core-to -Core Workshop on “New -Concept Spintronic Devices”****Chair:**Hideo Ohno**Date**

November 19, 2016 - November 20, 2016

**Workshop description**

This workshop will cover especially new concepts of spintronic devices including magnon transport, Heusler alloy, Domain wall motion, Exchange coupling, magnetic tunnel junction, spin orbit effect and etc.

**Local staffs and Secretariat in all workshops**

Local staffs: M. Shirai, F. Matsukura, K. Itoh, S. Fukami, and M. Kohda

Secretariat: N. Sato

**Sponsors**

Laboratory for Nanoelectronics and Spintronics, RIEC, Tohoku University

WPI-Advanced Institute for Materials Research (WPI-AIMR), Tohoku University

Center for Spintronics Integrated Systems (CSIS), Tohoku University

Center for Innovative Integrated Electronic Systems (CIES), Tohoku University

JSPS Core-to-Core Program

Graduate program in Spintronics (GP-Spin), Tohoku University

Center for Spintronics Research Network (CSRN), Tohoku University



Core-to-Core Program



Graduate Program  
in Spintronics

CSRN Tohoku University  
Center for Spintronics Research  
Network

#### Venue

Laboratory for Nanoelectronics and Spintronics, Research Institute of Electrical Communication, Tohoku University

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[↑ PAGE TOP](#)

